

FIG. 3

The diagram shows a control system 50 enclosed in a rectangular box. At the top, an arrow labeled "FROM BUTTONS 54" points into the system, and an arrow labeled "TO DISPLAY 56" points out from the top. Inside the box, there is an "OSCILLATOR" block (92) and a "MEMORY" block (89), both connected to a central "CPU" block (80). Below the CPU is an "A/D, D/A" block (82). A "MULTIPLEXING" block (88) is at the bottom. Arrows indicate data flow: from the CPU to the A/D, D/A block (81), from the A/D, D/A block to the CPU (85), from the CPU to the MULTIPLEXING block (84), and from the A/D, D/A block to the MULTIPLEXING block (1). A power supply line "FROM BATTERY 52" enters the box from the right, with a branch (5) going to the CPU and another branch (86) going to the MULTIPLEXING block. The MULTIPLEXING block has 16 output lines (90) pointing downwards, labeled "TO ELECTRODES 60".

FIG. 4

SWITCHING

94

96

98

120

124

22

100

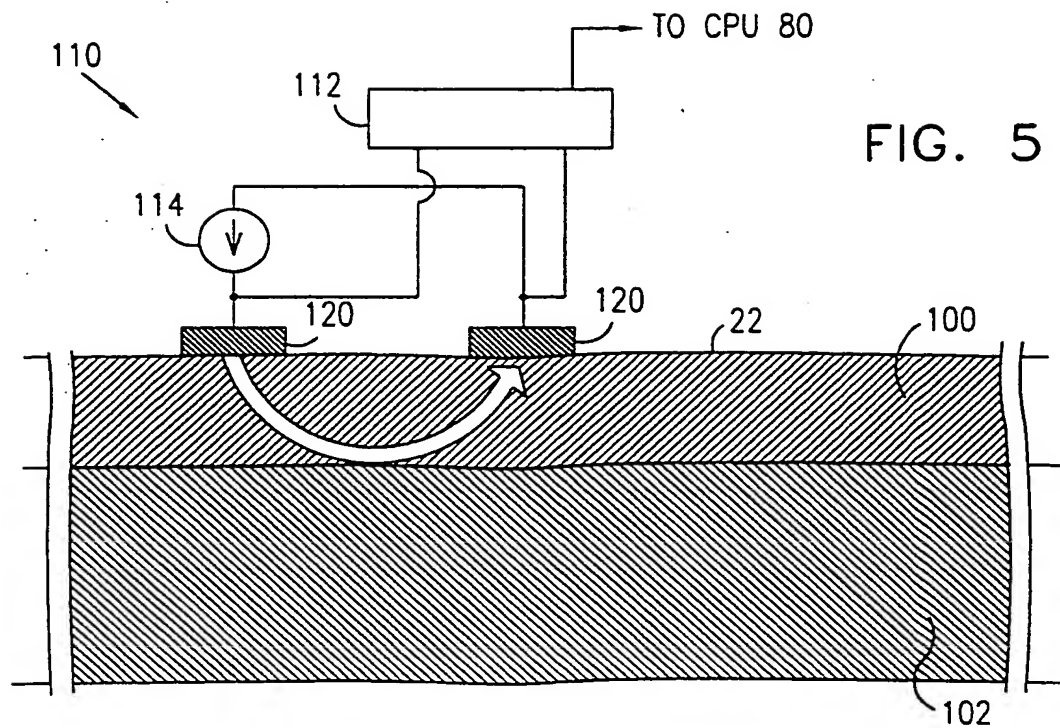


FIG. 5

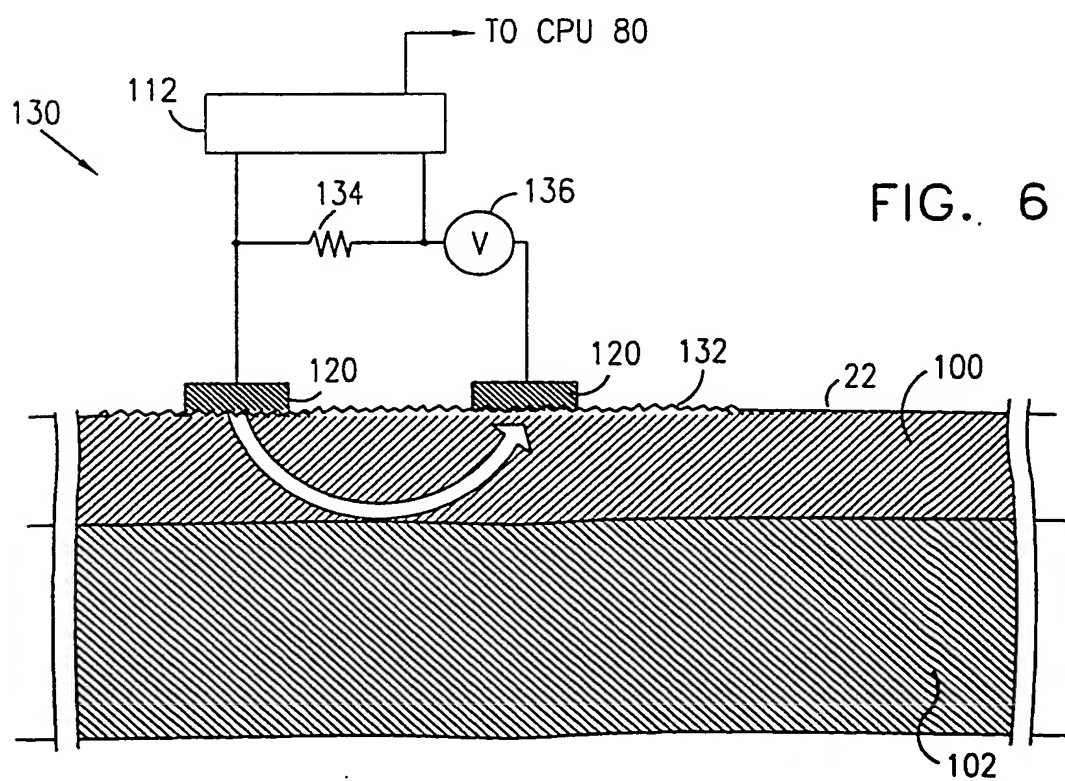


FIG. 6

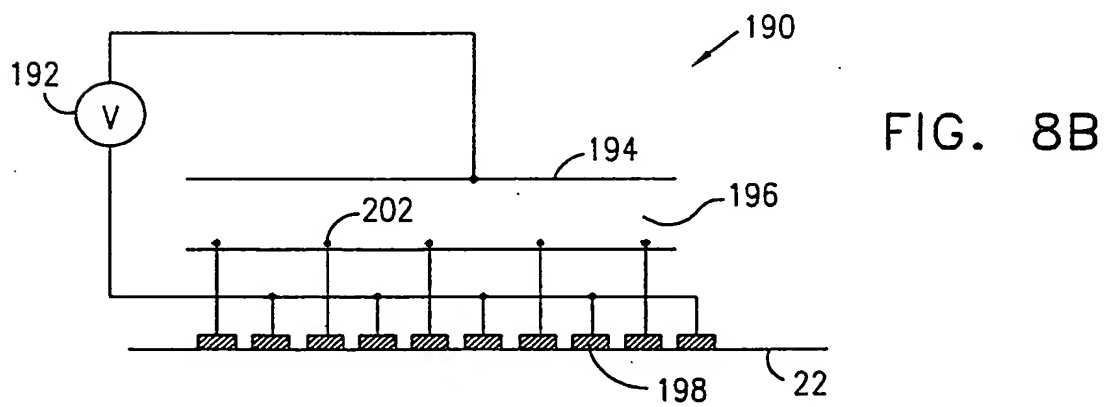
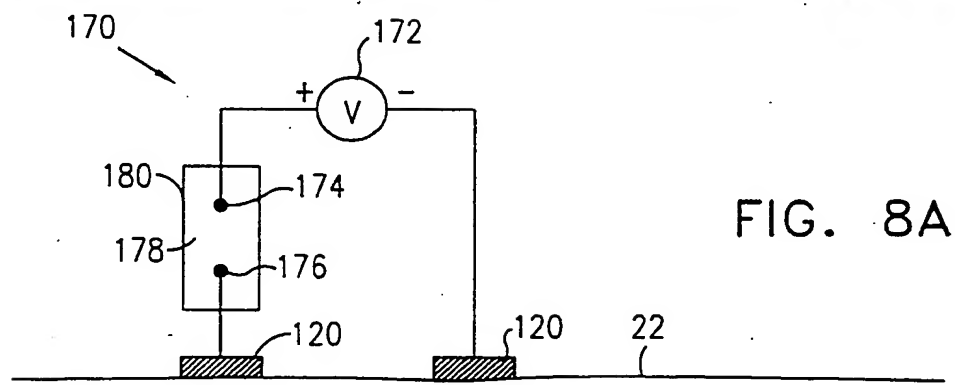
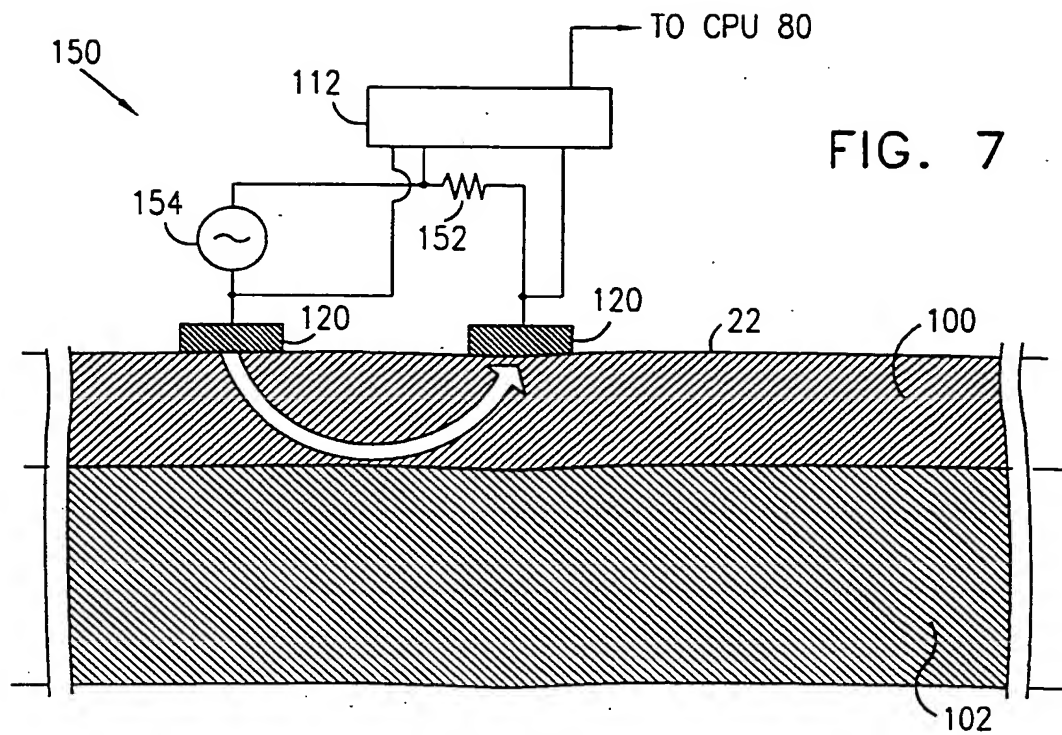


FIG. 9

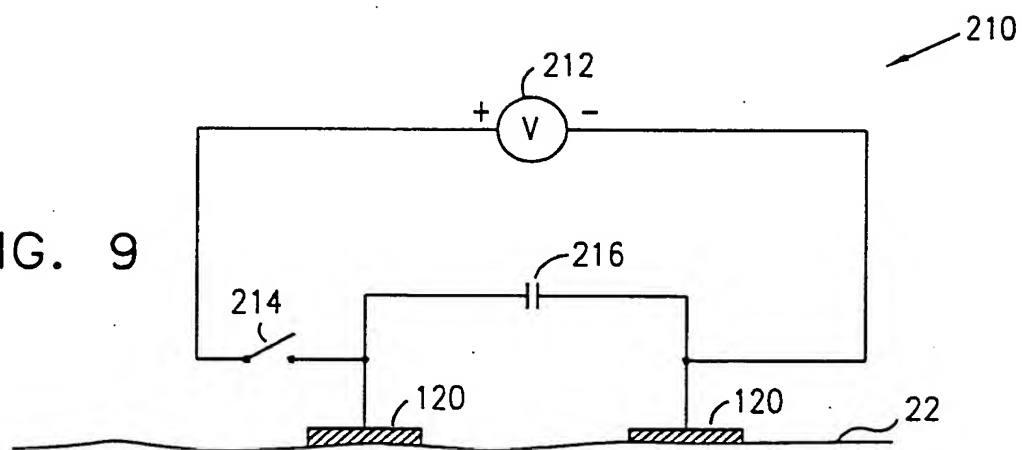


FIG. 10

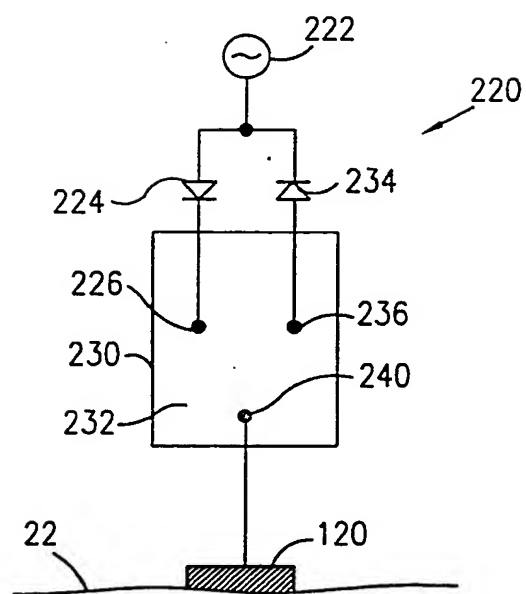


FIG. 11A

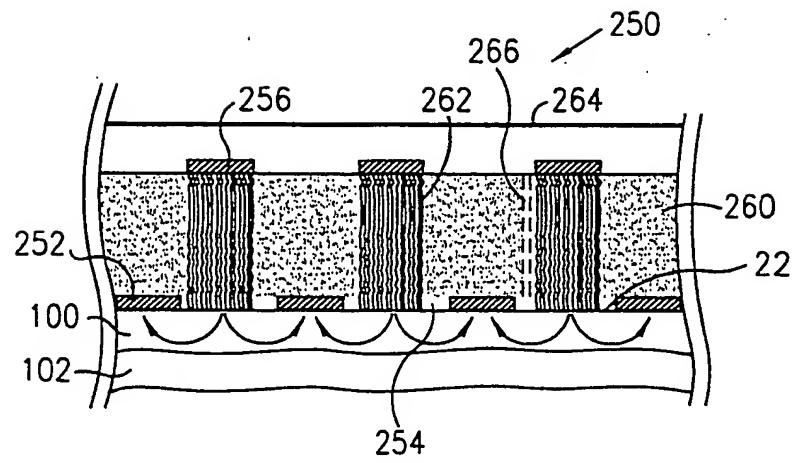
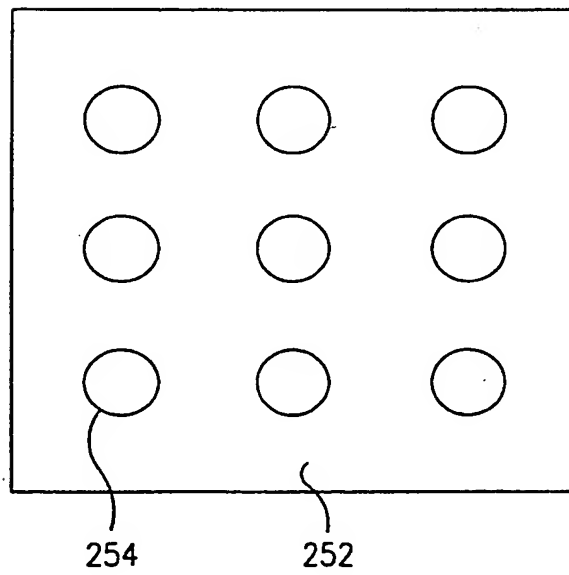


FIG. 11B



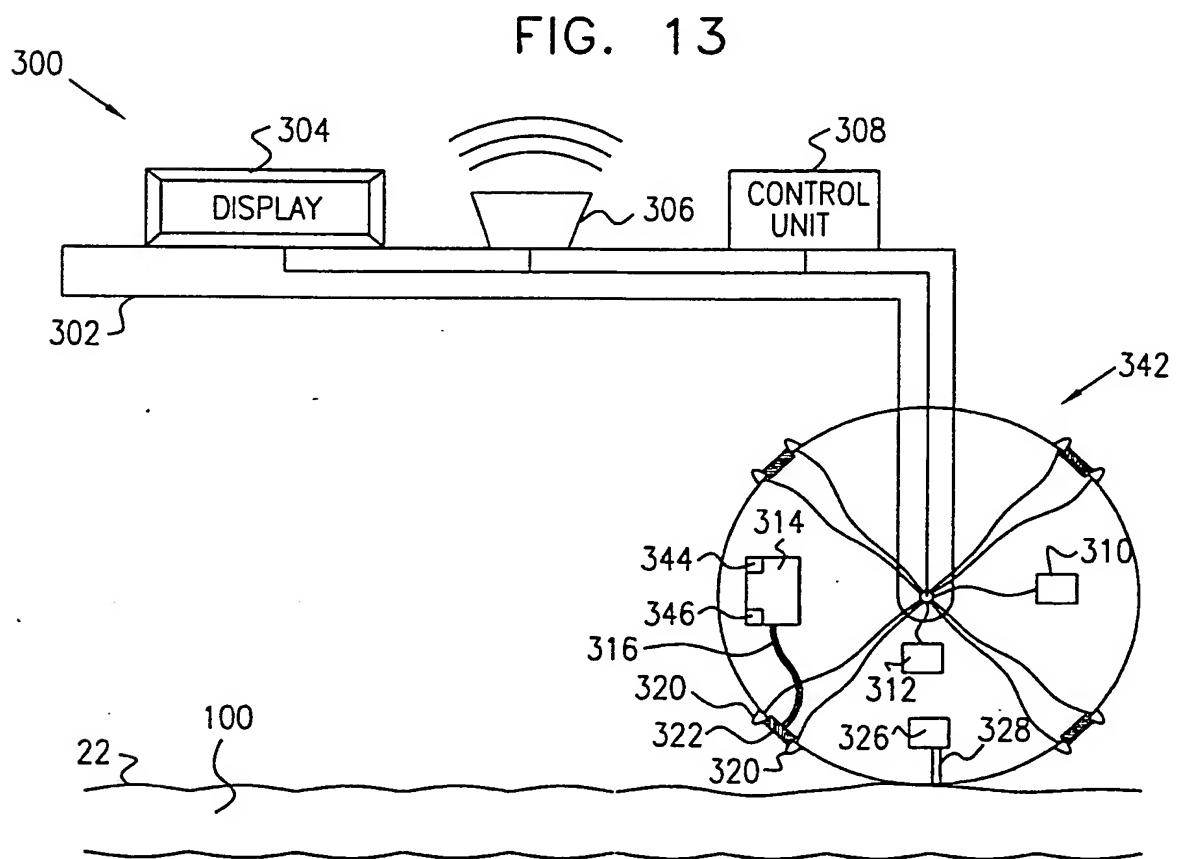
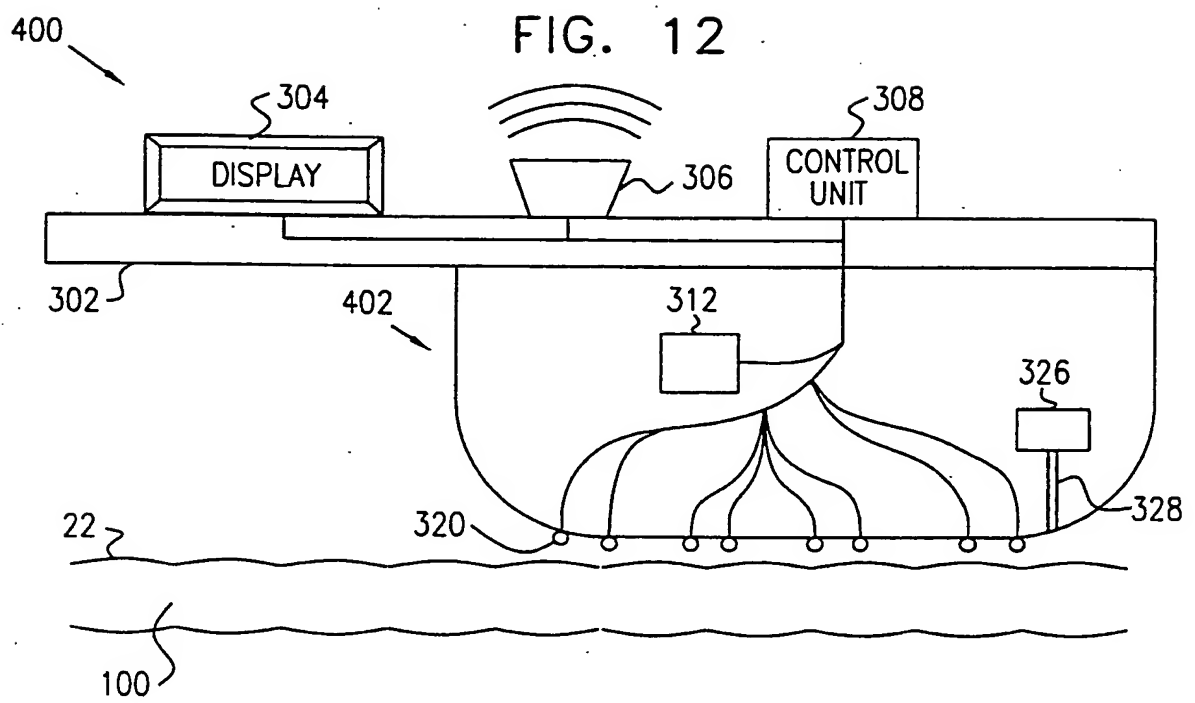


FIG. 14

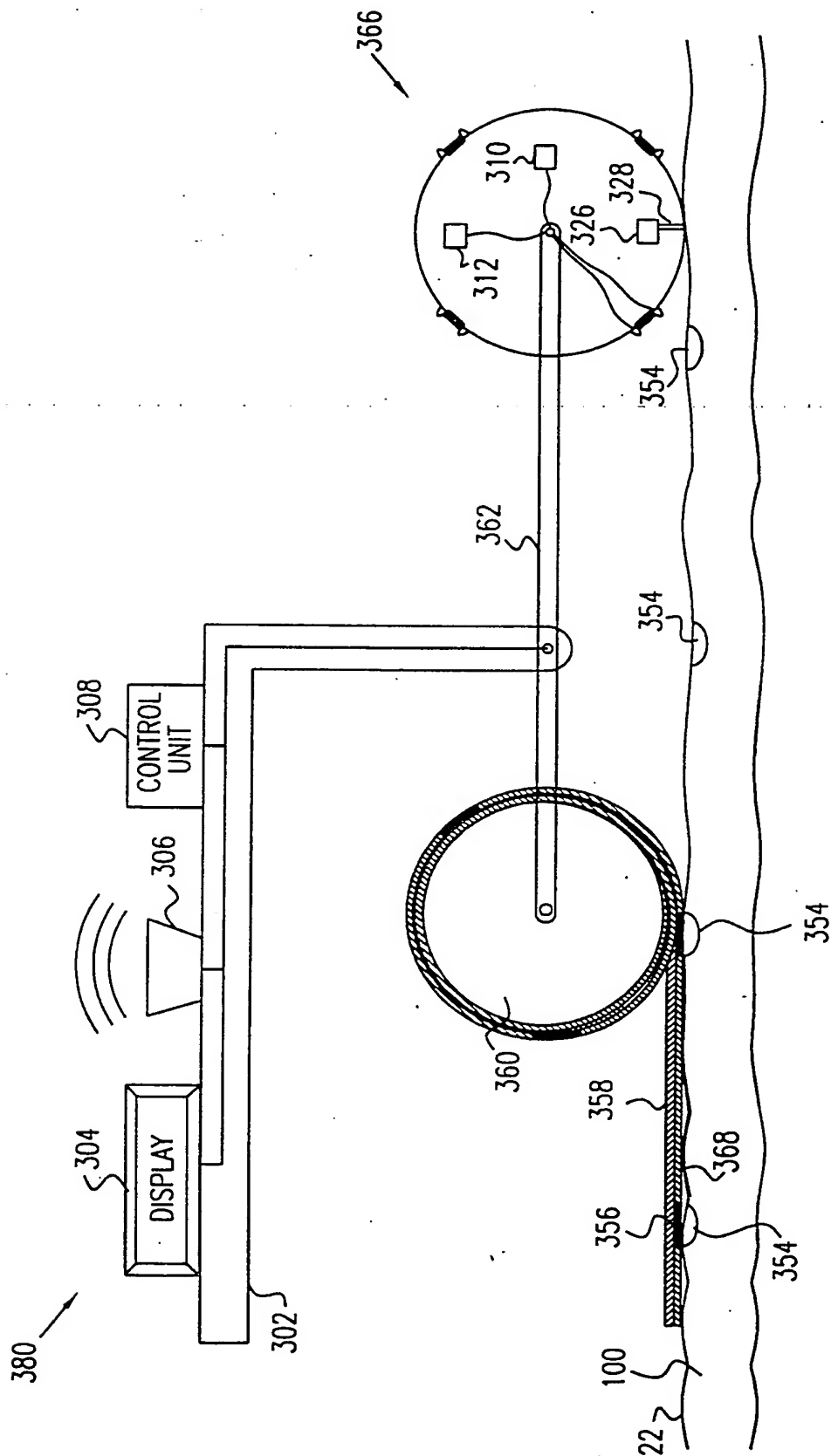
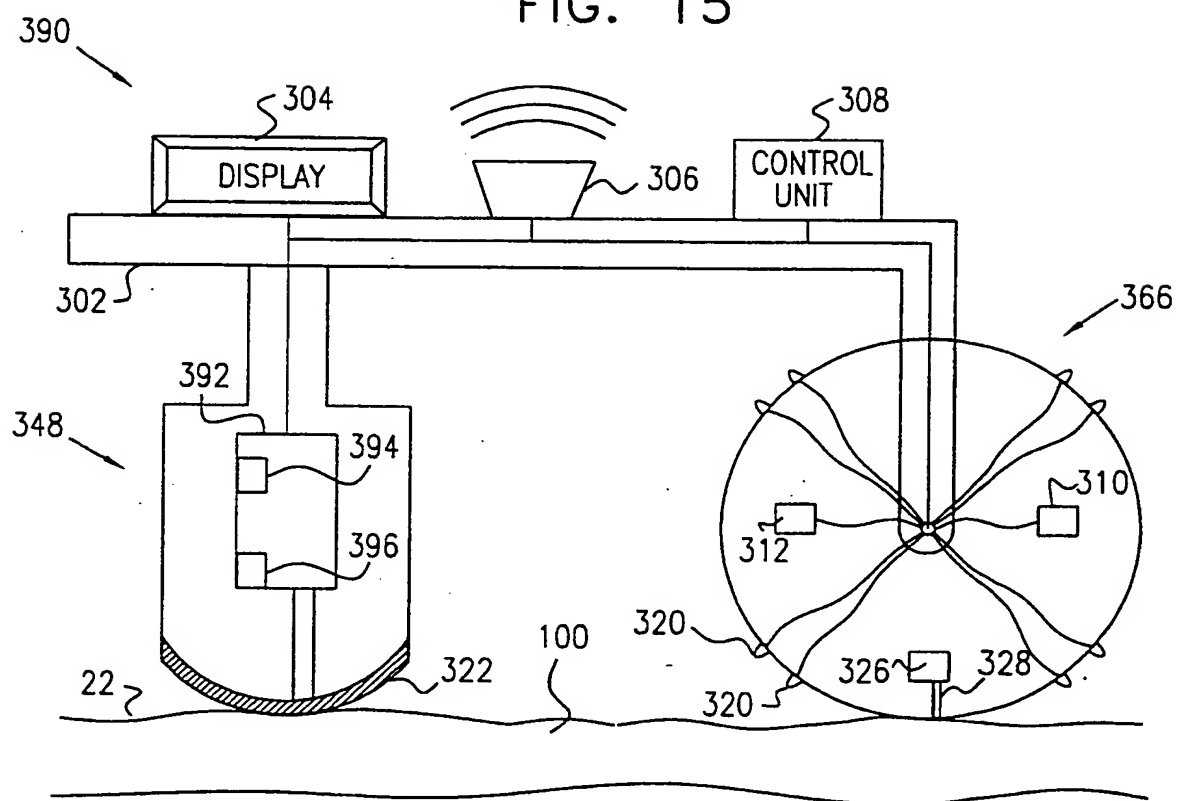




FIG. 15



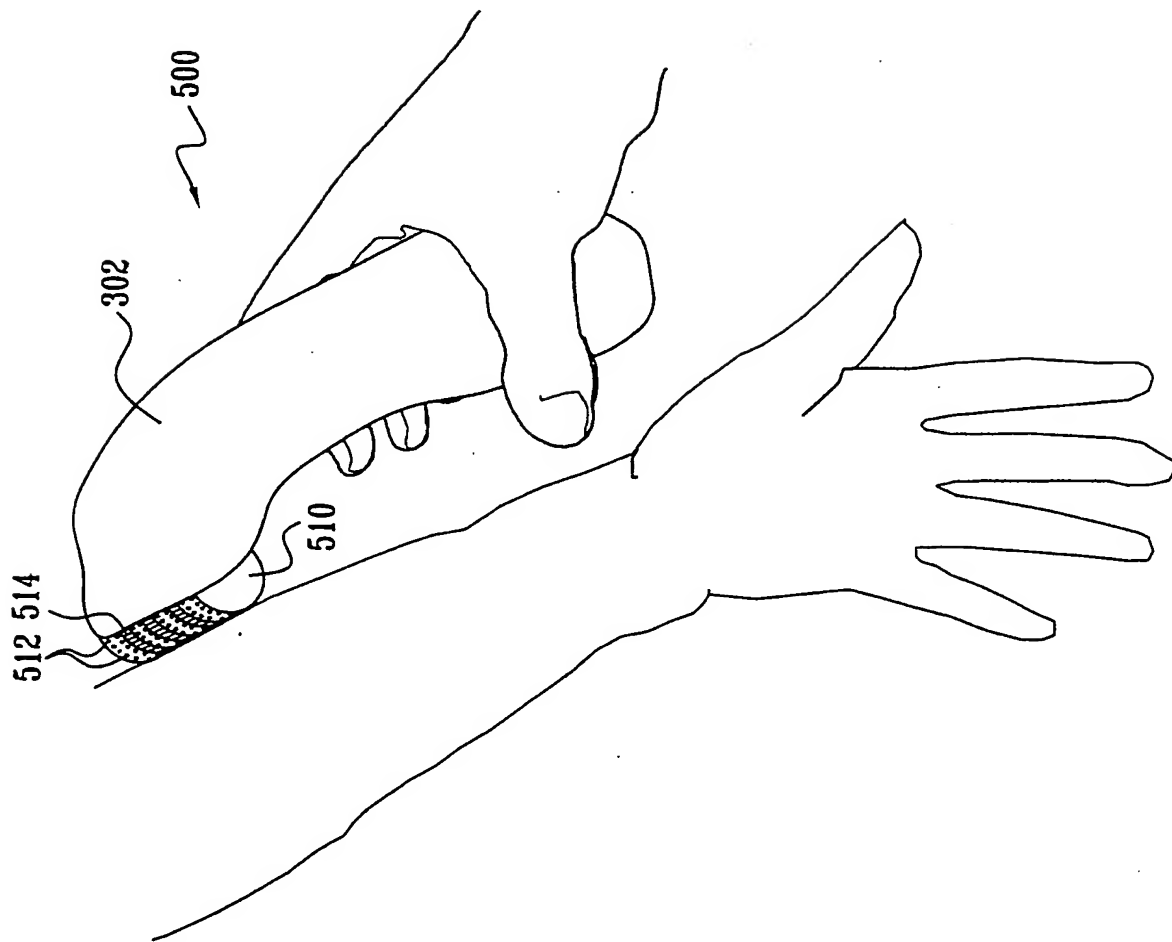


FIG. 16

FIG. 17A

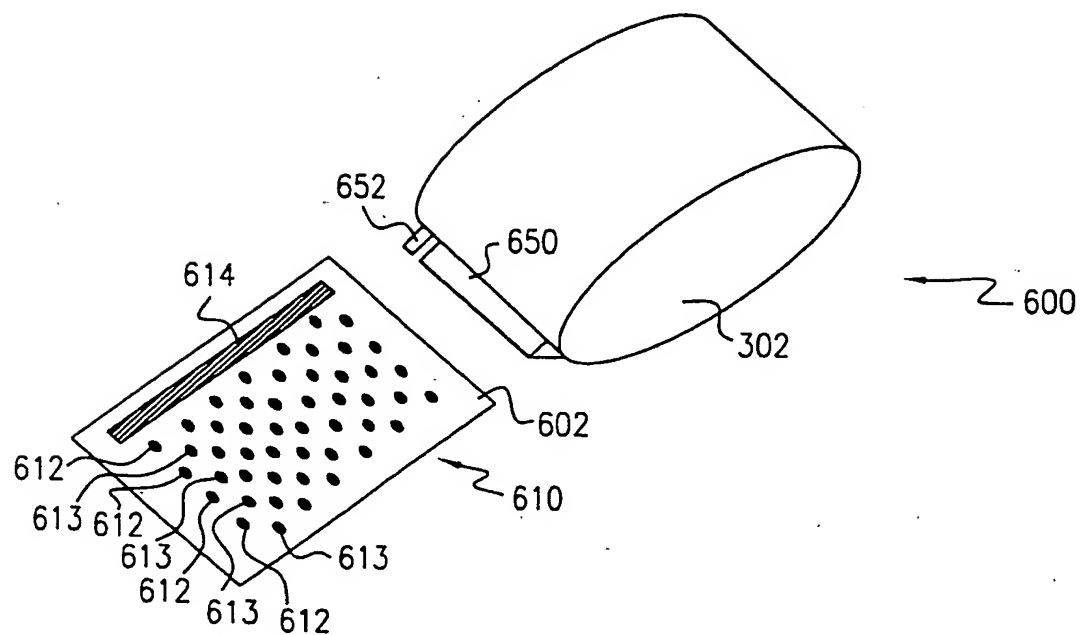


FIG. 17B

